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SHIFT REGISTER CHAIN FOR TRIMMING GENERATORS FOR AN INTEGRATED SEMICONDUCTOR APPARATUS

ABSTRACT

An integrated semiconductor apparatus includes at least one fuse box device that includes fuses for storing trimming data, a fuse box trimming output, and a timer emitting a clock signal. The apparatus also includes a parallel/serial converter that is connected to the fuses and to the timer. The parallel/serial converter is configured to read, in parallel, the trimming data from the fuses and to emit, in serial, the trimming data from the fuse block trimming output based on the clock signal. The apparatus also includes generators. Each generator generates a generator signal and includes a trimming unit that has a trimming signal input. The trimming unit is configured to trim the generator signal of the generator based on the trimming data received. The generator also includes a trimming signal output and memory flip-flops that connect the trimming input signal to the trimming output signal. Each of the memory flip-flops connects each trimming unit to the fuse block trimming output to form a shift register chain for serial transmission of the trimming data from the fuse block device to the generators.

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